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APPLICANTS

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** CONTINUING DATA ***** *KIBR 07*

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 17	INDEPENDENT CLAIMS 3
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Verified and Acknowledged *KIBR 07* *KK*
 Examiner's Signature Initials

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TITLE
 Simulation method, simulation program, and semiconductor device manufacturing method each employing boundary conditions

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